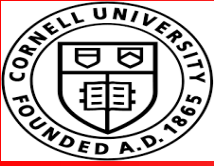


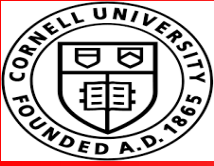
TFPX module RH0017 testing at Cornell 06-14-23



Test program



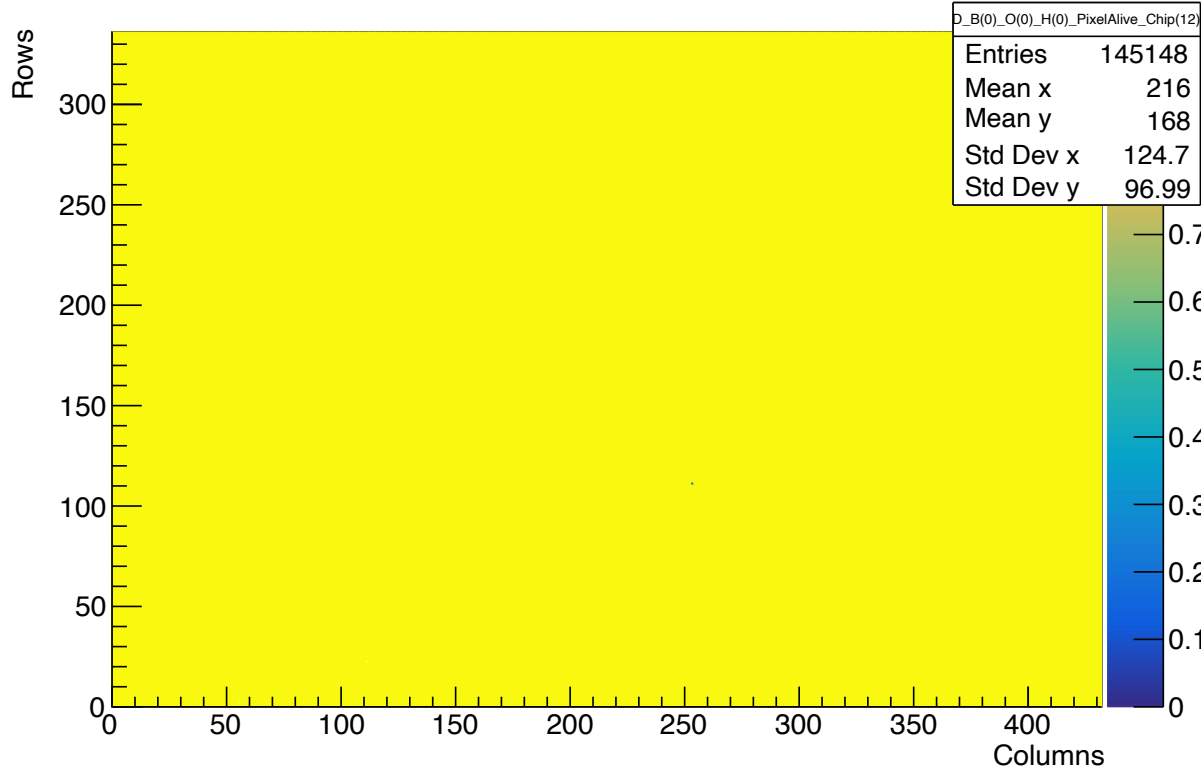
- 1) Pixelalive @ GDAC=500
- 2) Thradj @3500 e-
- 3) *threqu (default, DoNsteps=0)*
- 4) *Scurve*
- 4a) *optional [Noise, Scurve]*
- 5) thradj @1500 e-
- 6) **threqu with DoNsteps=3**
- 7) Scurve
- 7a) *optional [Noise, Scurve]*
- 8) *thradj @1200 e-*
- 9) **threqu with DoNsteps=2**
- 10) *Scurve*
- 10a) *optional [Noise, Scurve]*
- 11) thradj @1000 e-
- 12) **threqu with DoNsteps=2**
- 13) Scurve
- 14) Noise
- 15) Scurve
- 16) thradj @900 e-
- 17) *threqu*
- 18) Scurve
- 19) [Noise]
- 20) Scurve
- 21) thradj @800
- 22) *threqu*
- 23) Scurve
- 24) [Noise]
- 25) Scurve
- 26) *Gain*



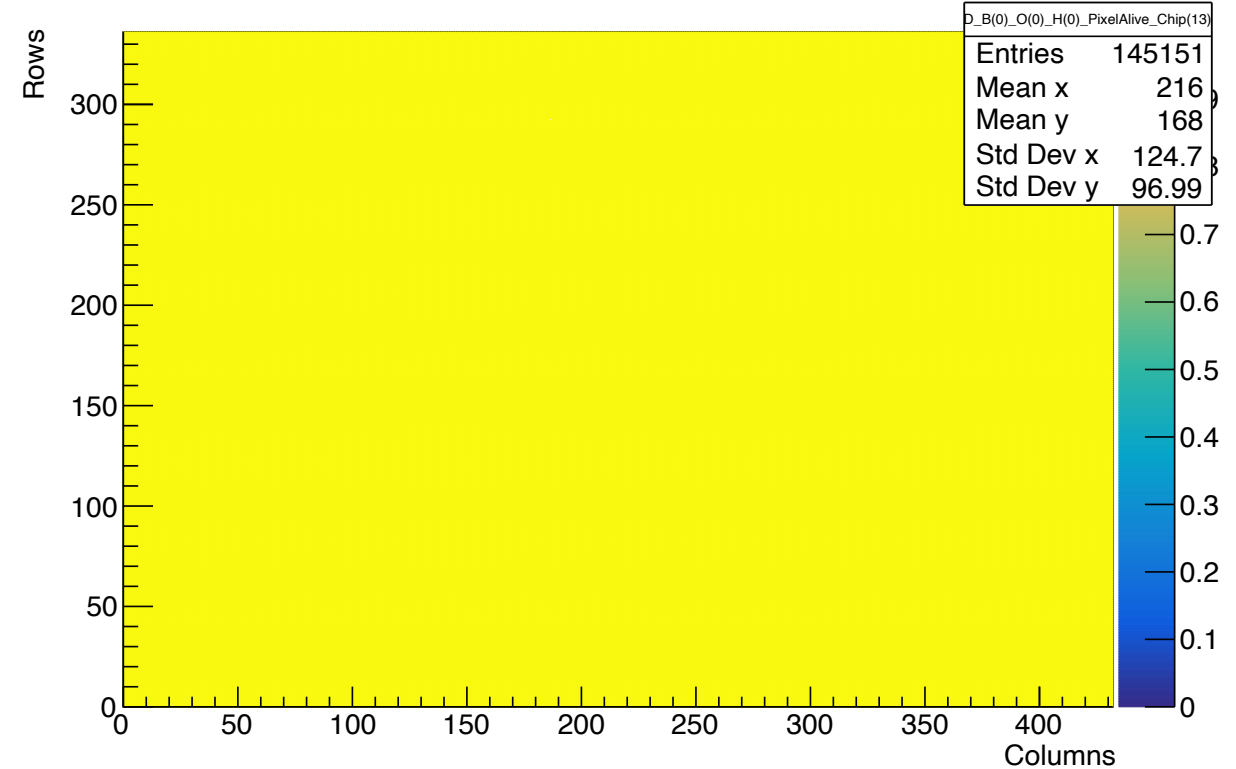
Pixel Alive – pre-test @ GDAC=500



D_B(0)_O(0)_H(0)_Pixel Alive_Chip(12)

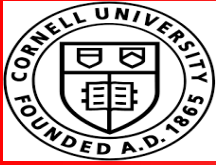


D_B(0)_O(0)_H(0)_Pixel Alive_Chip(13)

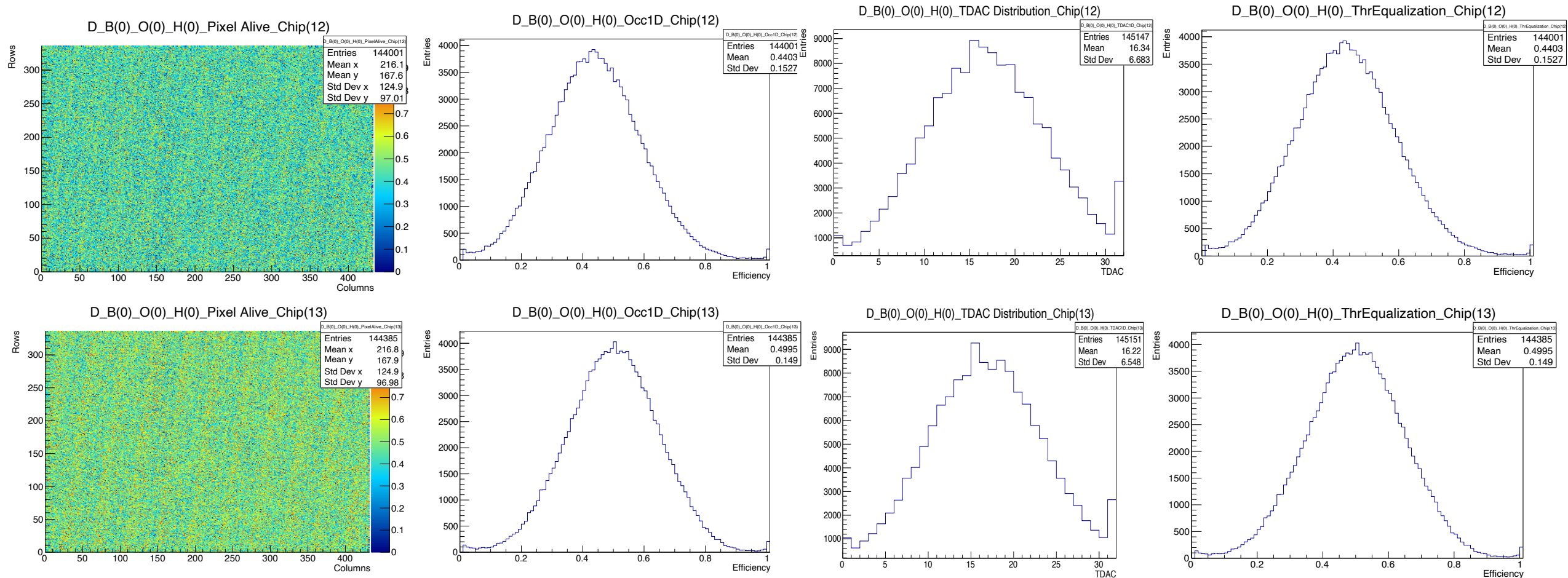


Run0000 pixelalive @GDAC 500, HV=-80V [3 min]

- Humidity: 50.90 %, Temp: 22.60 Celsius dewpoint calculated 11.6 C
- Cooling Base Plate Temp: 15.3C
- Pixel masked: 4(chip 12), 1(chip 13)

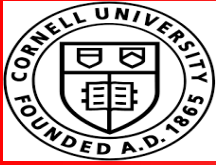


threq @3500e-, DoNSteps=0



Note the bands' structure: more visible for chip 13

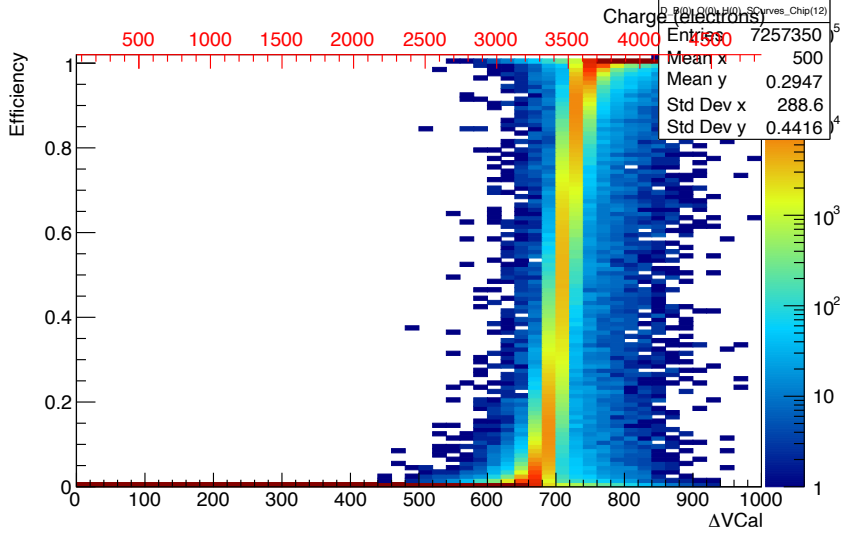
Masked pixels: 5,1; GDAC = 505,462



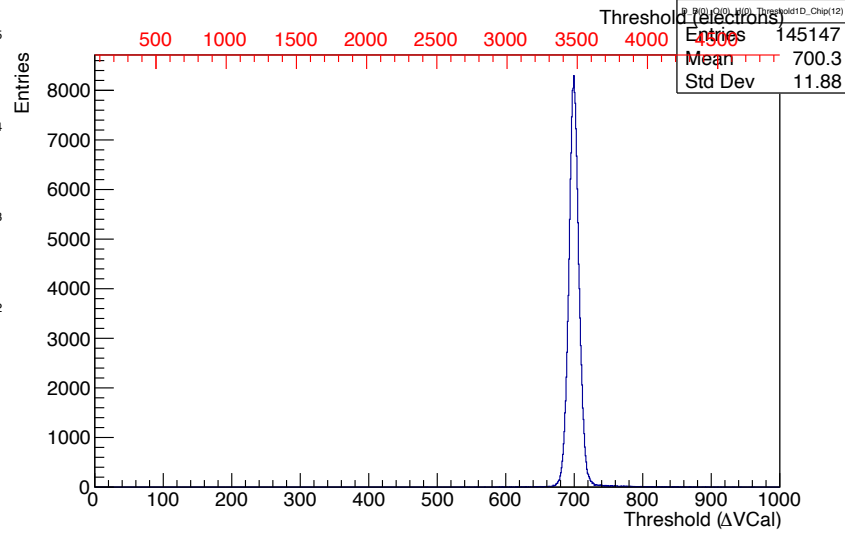
SCurve @3500e-



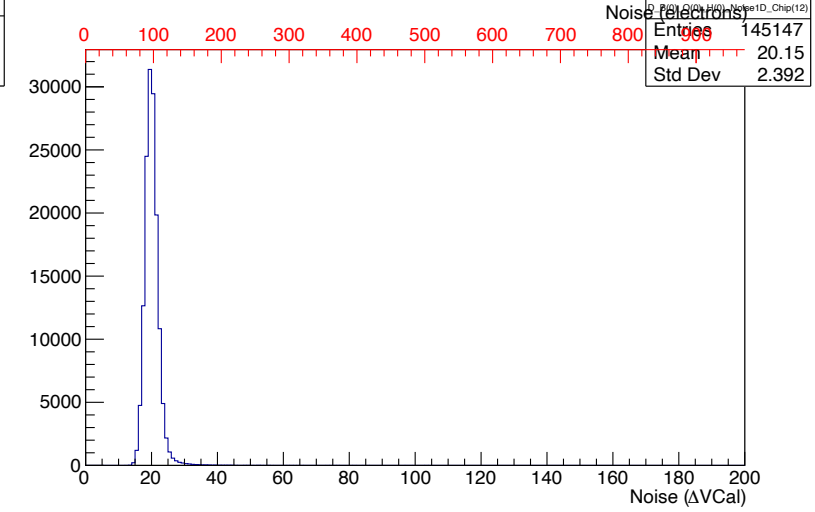
D_B(0)_O(0)_H(0)_SCurves_Chip(12)



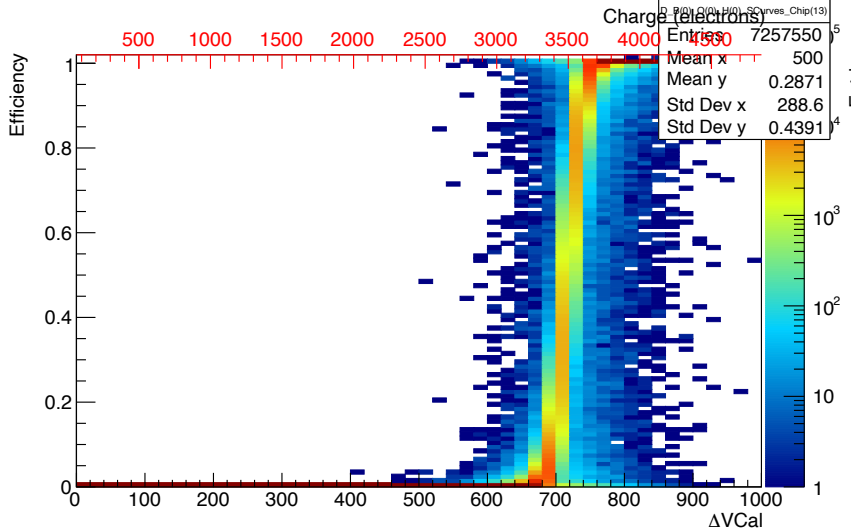
D_B(0)_O(0)_H(0)_Threshold Distribution_Chip(12)



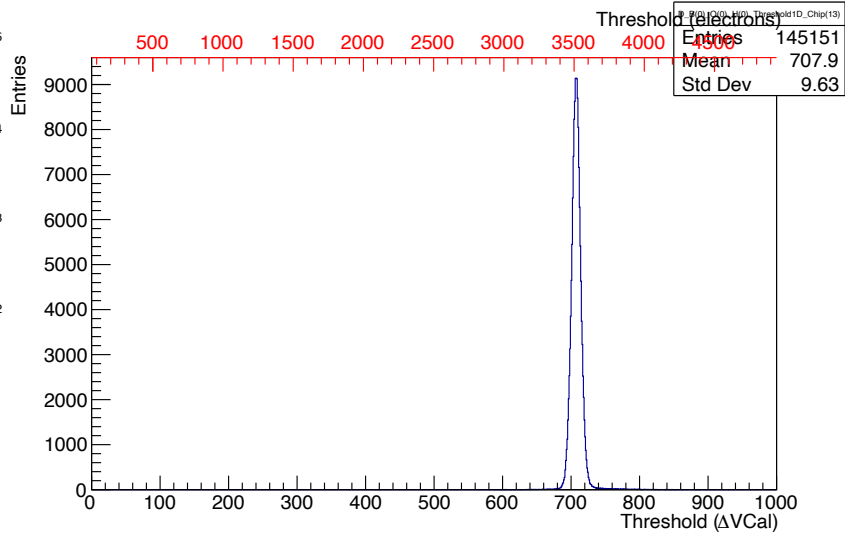
D_B(0)_O(0)_H(0)_Noise Distribution_Chip(12)



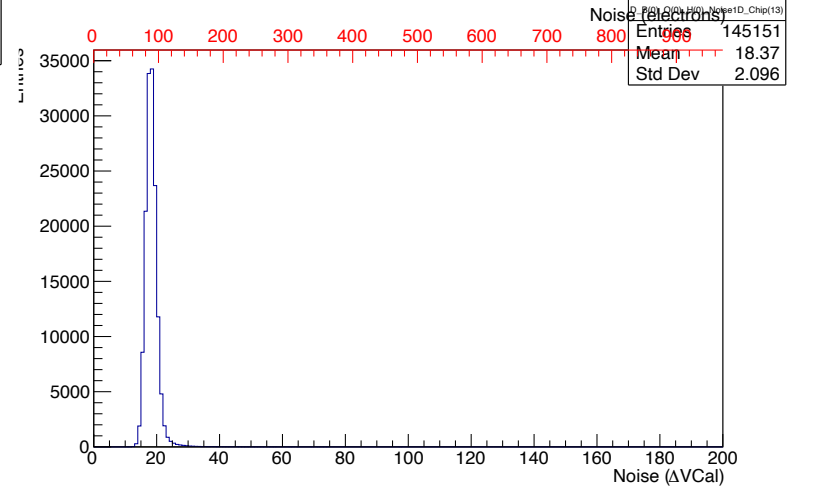
D_B(0)_O(0)_H(0)_SCurves_Chip(13)

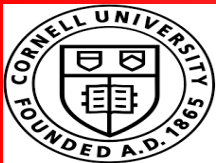


D_B(0)_O(0)_H(0)_Threshold Distribution_Chip(13)



D_B(0)_O(0)_H(0)_Noise Distribution_Chip(13)

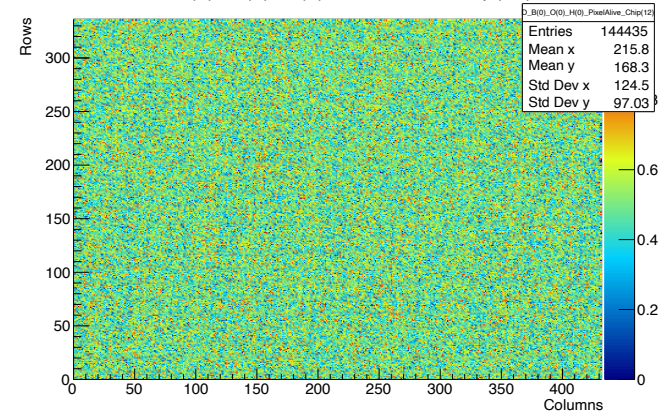




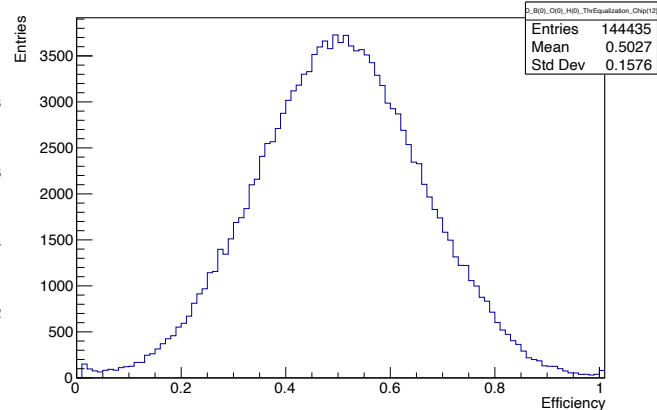
threqu @1500e-, DoNSteps=3



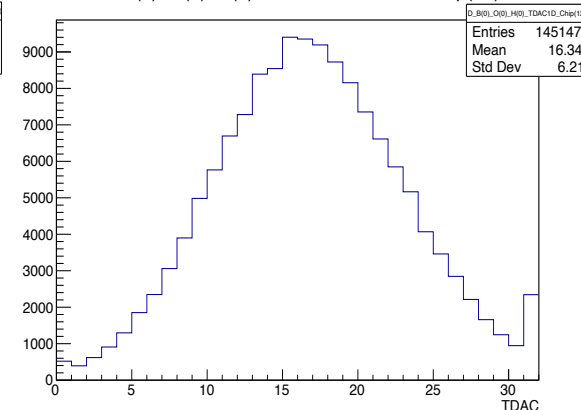
D_B(0)_O(0)_H(0)_Pixel Alive_Chip(12)



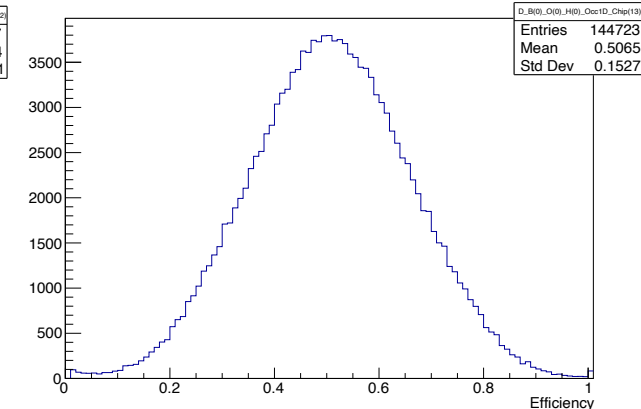
D_B(0)_O(0)_H(0)_ThrEqualization_Chip(12)



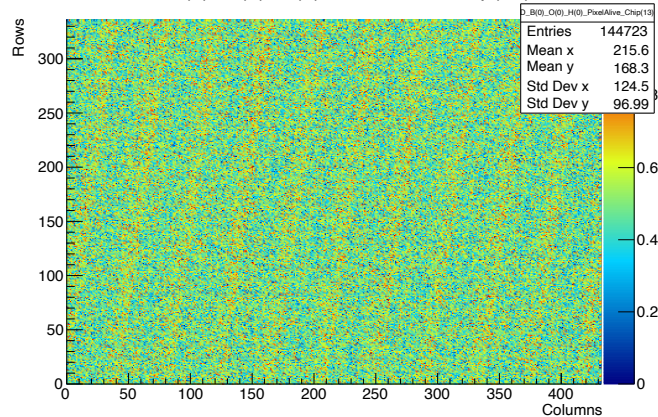
D_B(0)_O(0)_H(0)_TDAC Distribution_Chip(12)



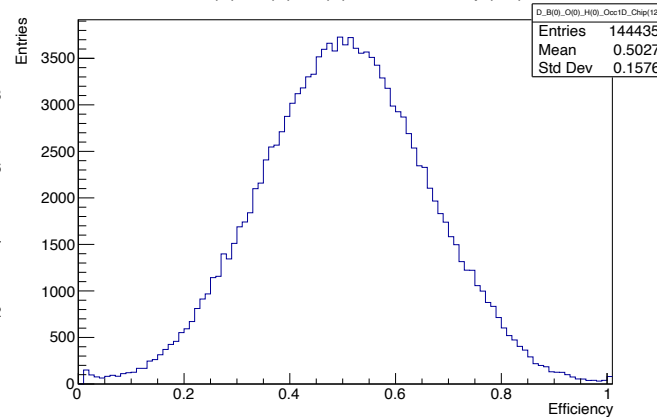
D_B(0)_O(0)_H(0)_Occ1D_Chip(13)



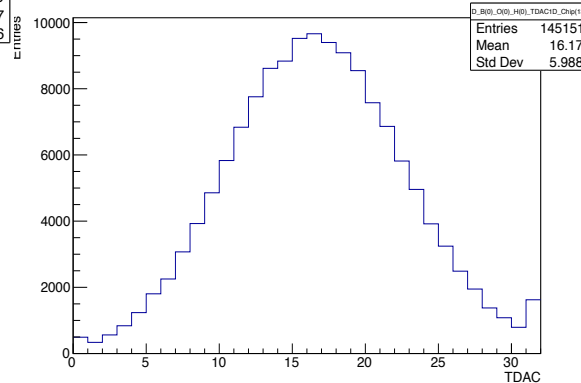
D_B(0)_O(0)_H(0)_Pixel Alive_Chip(13)



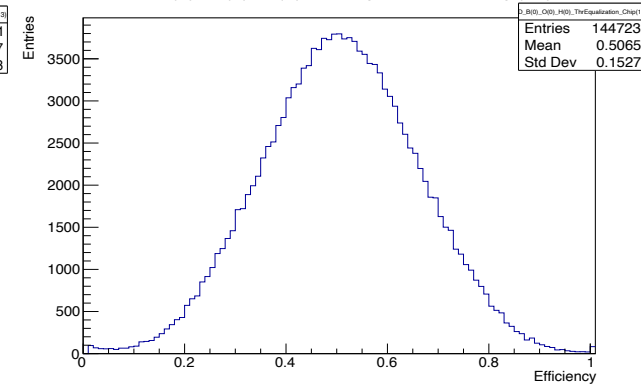
D_B(0)_O(0)_H(0)_Occ1D_Chip(12)

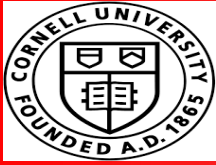


D_B(0)_O(0)_H(0)_TDAC Distribution_Chip(13)

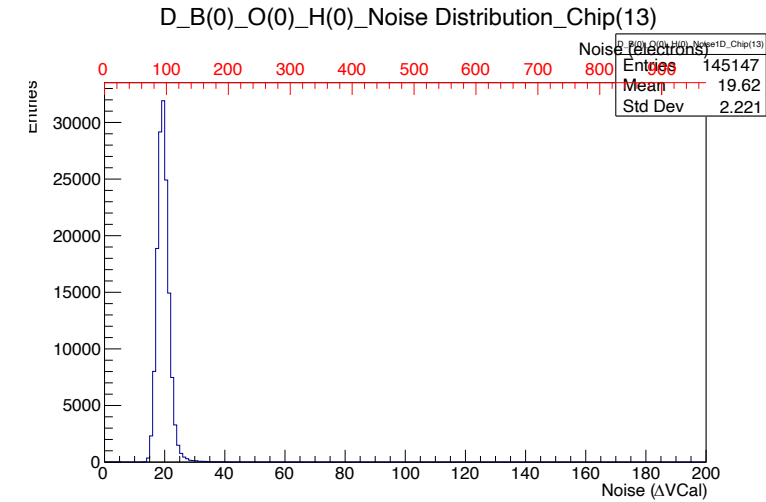
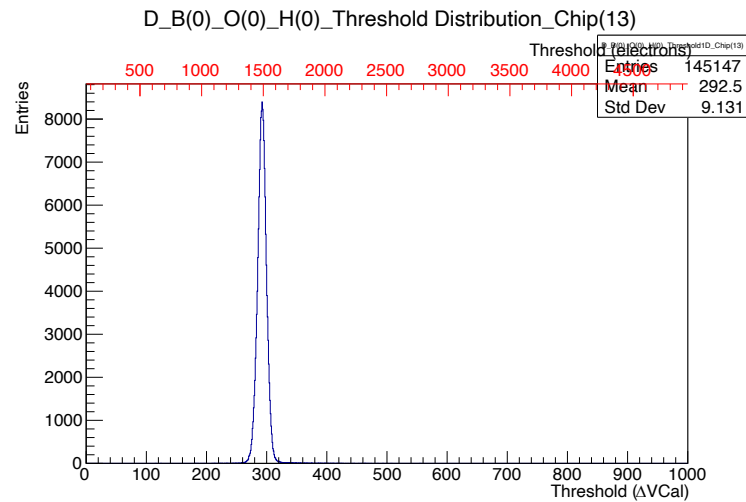
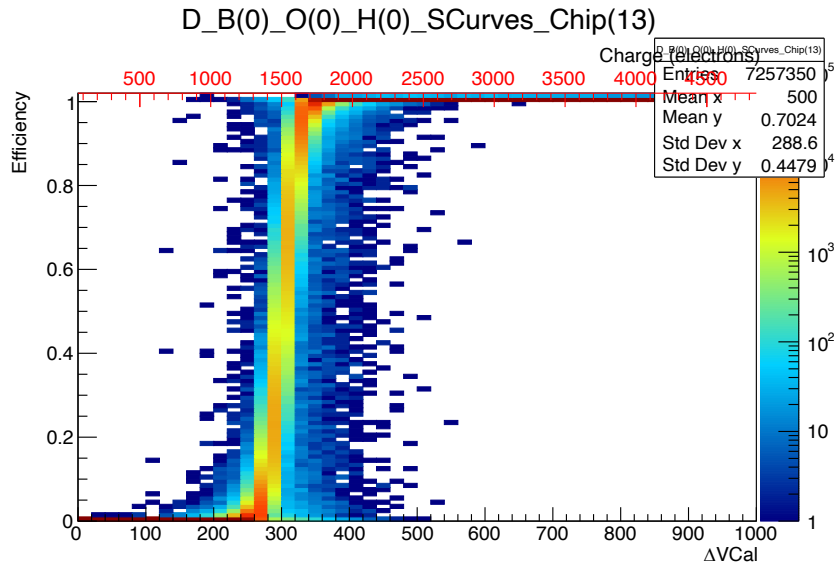
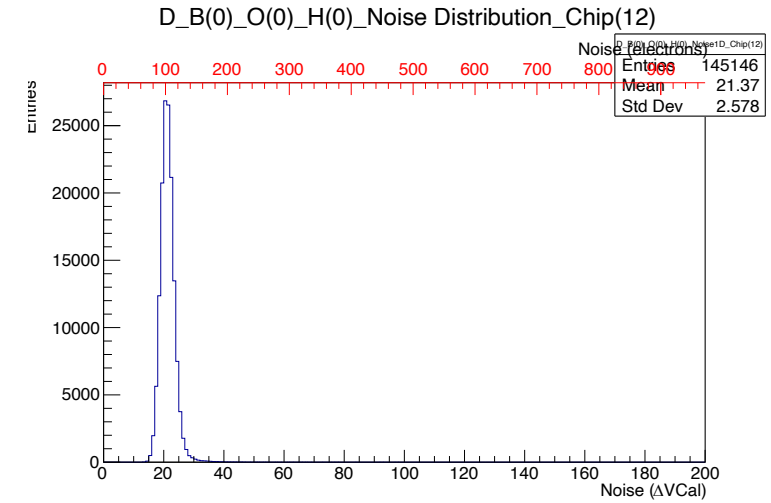
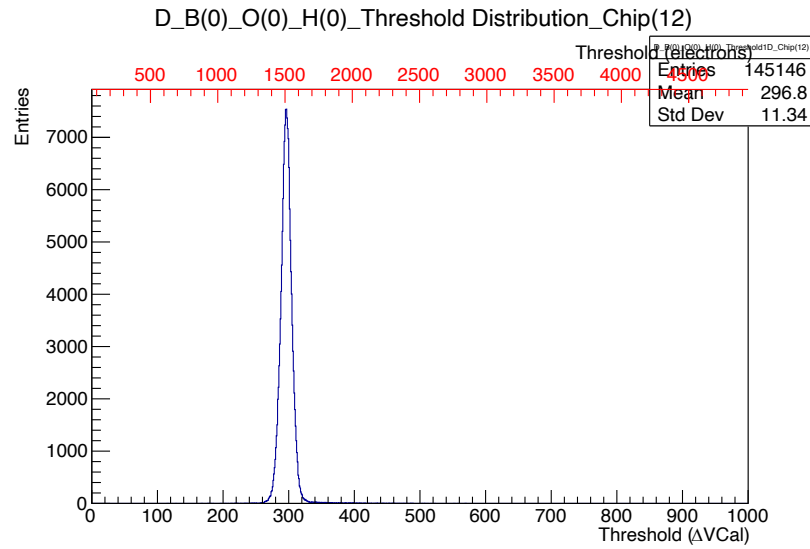
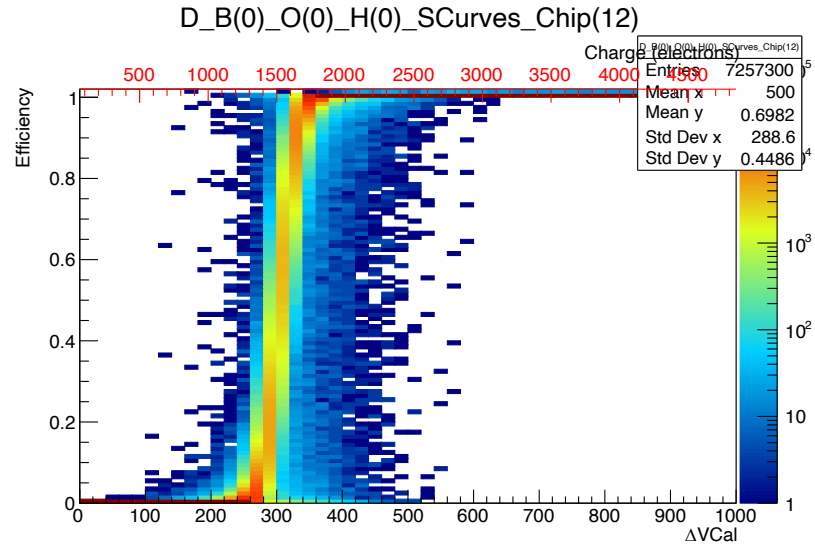


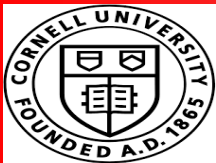
D_B(0)_O(0)_H(0)_ThrEqualization_Chip(13)





SCurve @1500e-

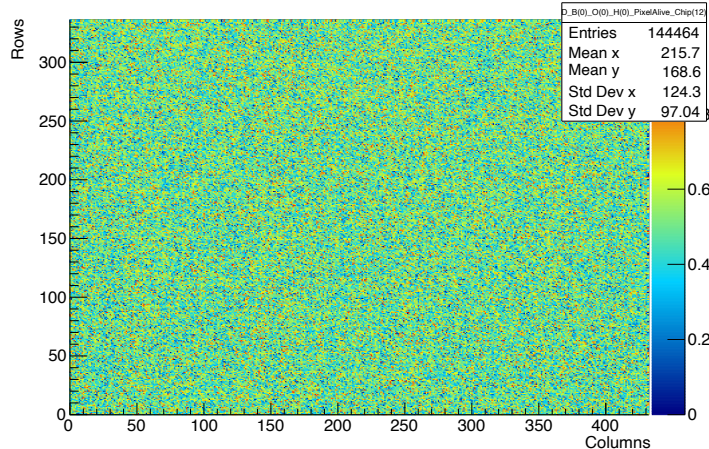




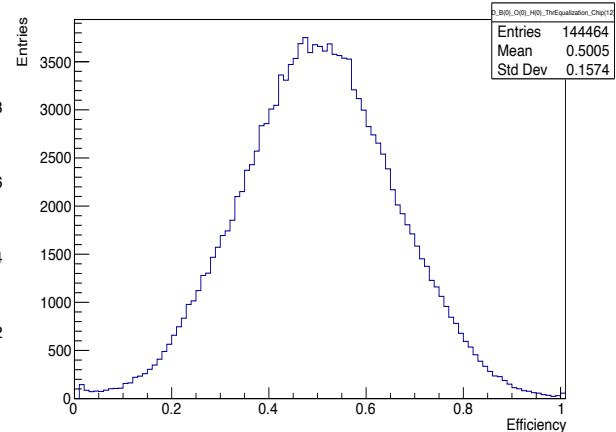
threqu @1200e-, DoNSteps=2



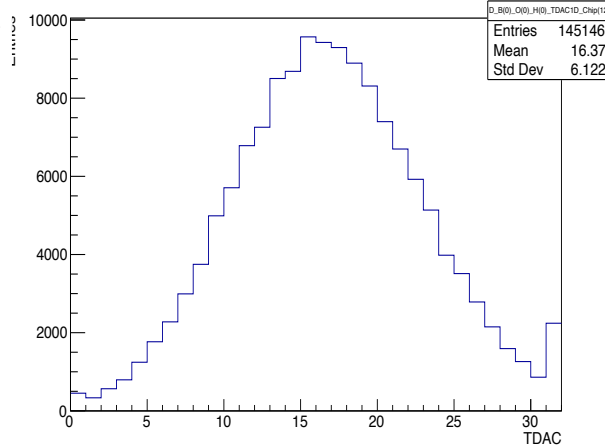
D_B(0)_O(0)_H(0)_Pixel Alive_Chip(12)



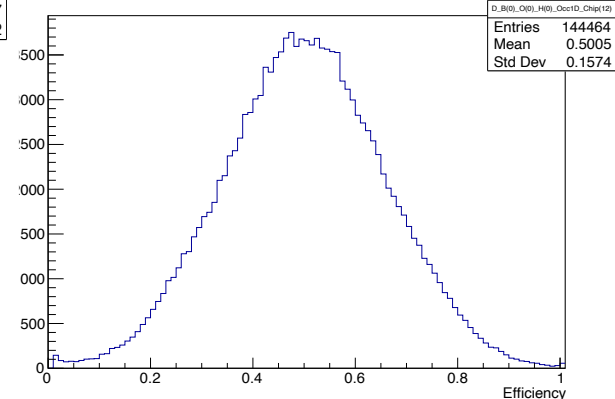
D_B(0)_O(0)_H(0)_ThrEqualization_Chip(12)



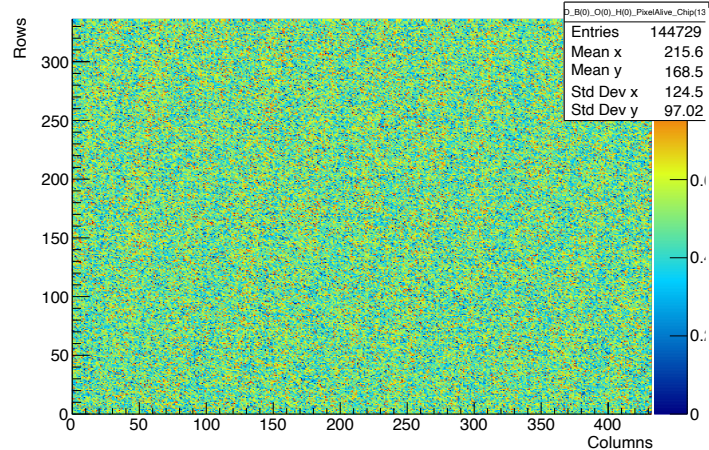
D_B(0)_O(0)_H(0)_TDAC Distribution_Chip(12)



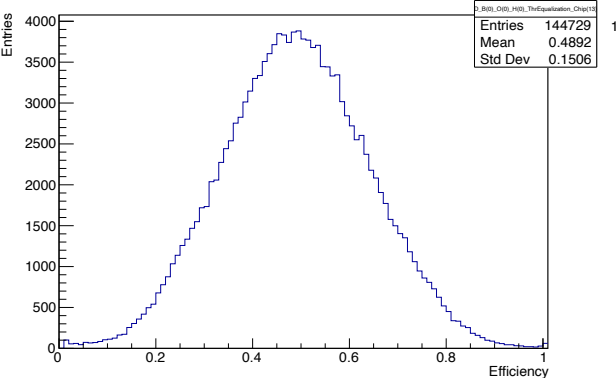
D_B(0)_O(0)_H(0)_Occ1D_Chip(12)



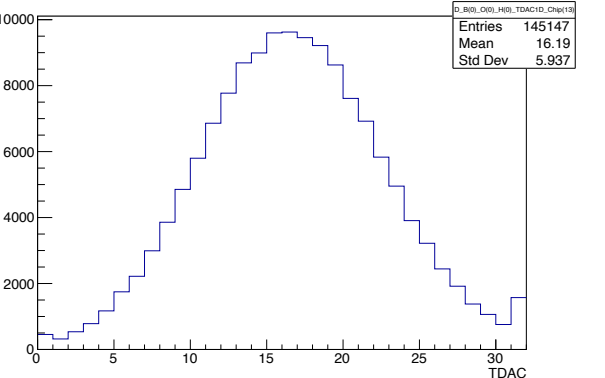
D_B(0)_O(0)_H(0)_Pixel Alive_Chip(13)



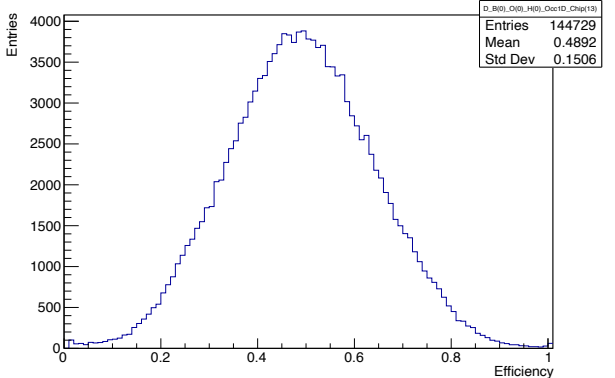
D_B(0)_O(0)_H(0)_ThrEqualization_Chip(13)

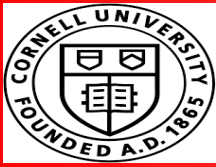


D_B(0)_O(0)_H(0)_TDAC Distribution_Chip(13)



D_B(0)_O(0)_H(0)_Occ1D_Chip(13)

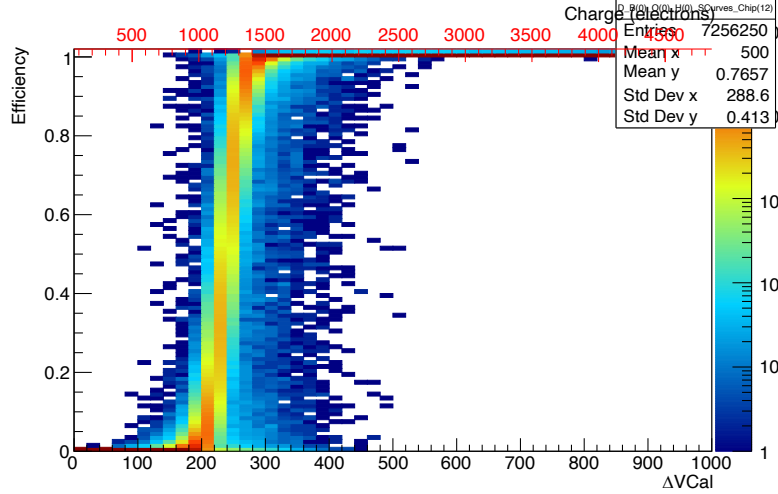




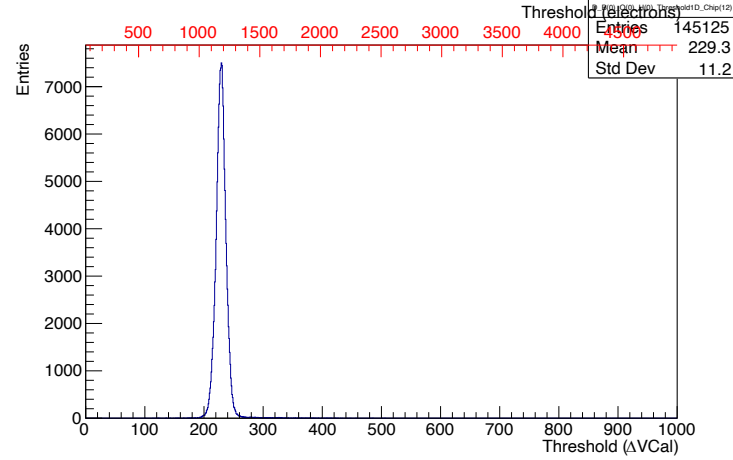
SCurve @1200e-



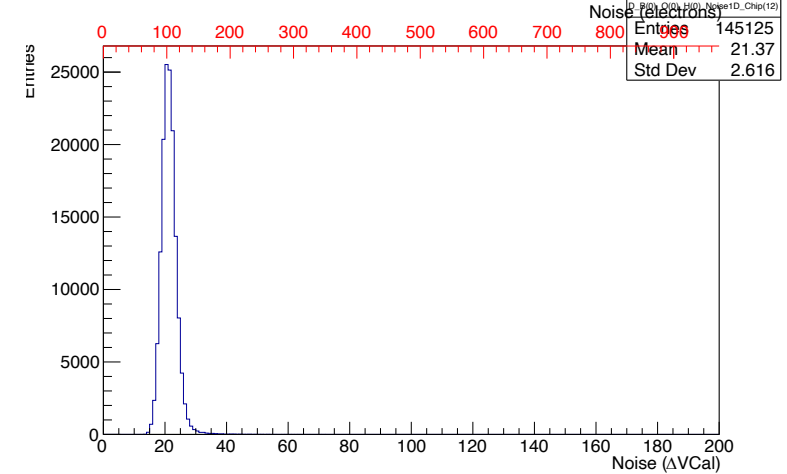
D_B(0)_O(0)_H(0)_SCurves_Chip(12)



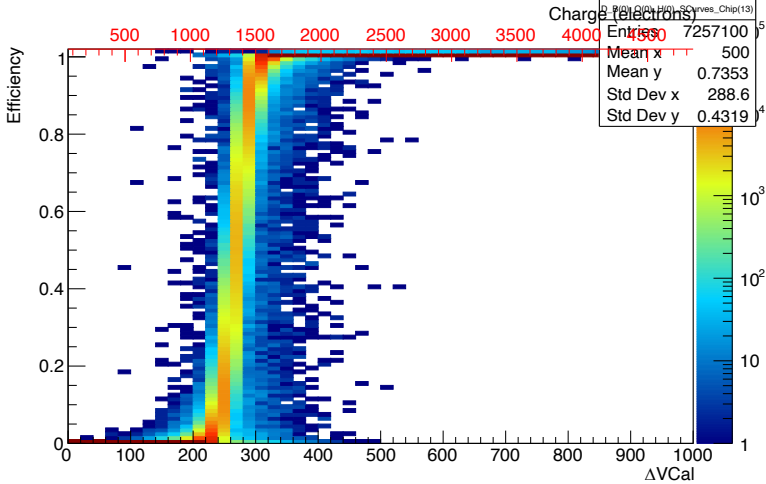
D_B(0)_O(0)_H(0)_Threshold Distribution_Chip(12)



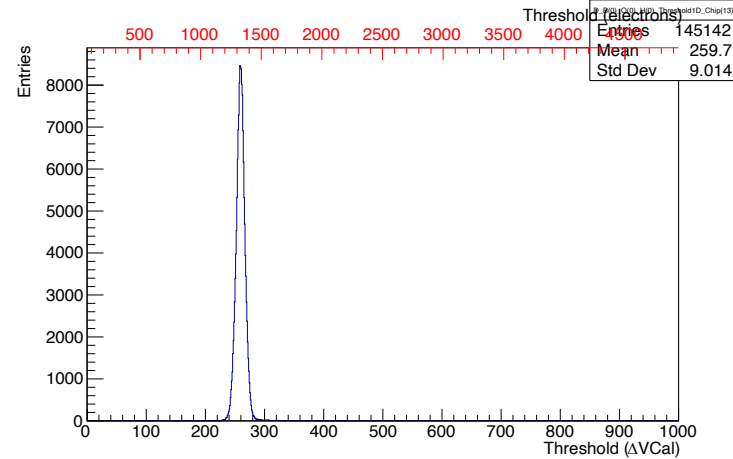
D_B(0)_O(0)_H(0)_Noise Distribution_Chip(12)



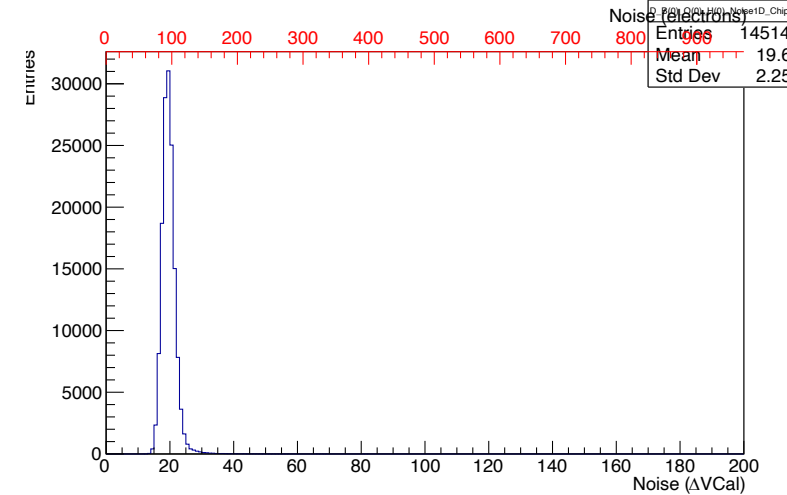
D_B(0)_O(0)_H(0)_SCurves_Chip(13)

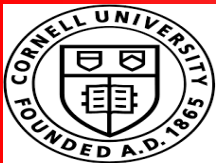


D_B(0)_O(0)_H(0)_Threshold Distribution_Chip(13)

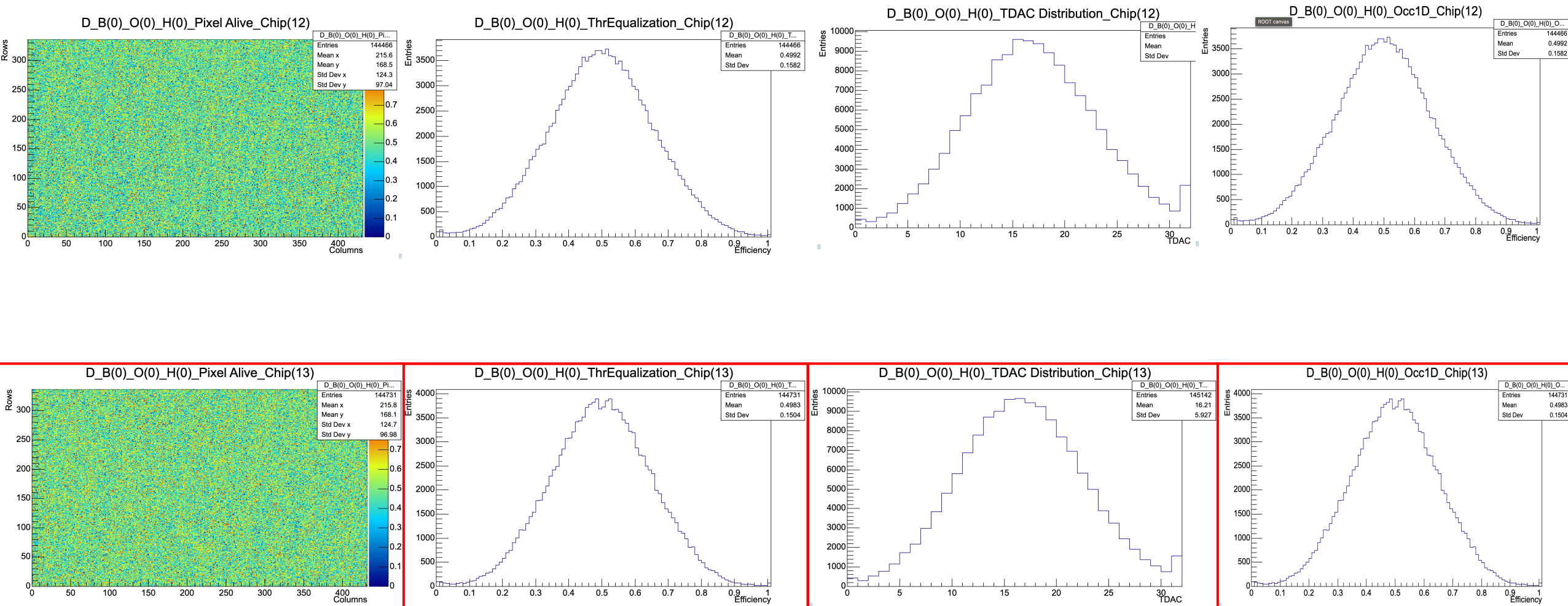


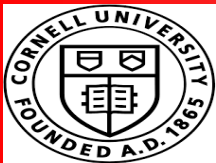
D_B(0)_O(0)_H(0)_Noise Distribution_Chip(13)



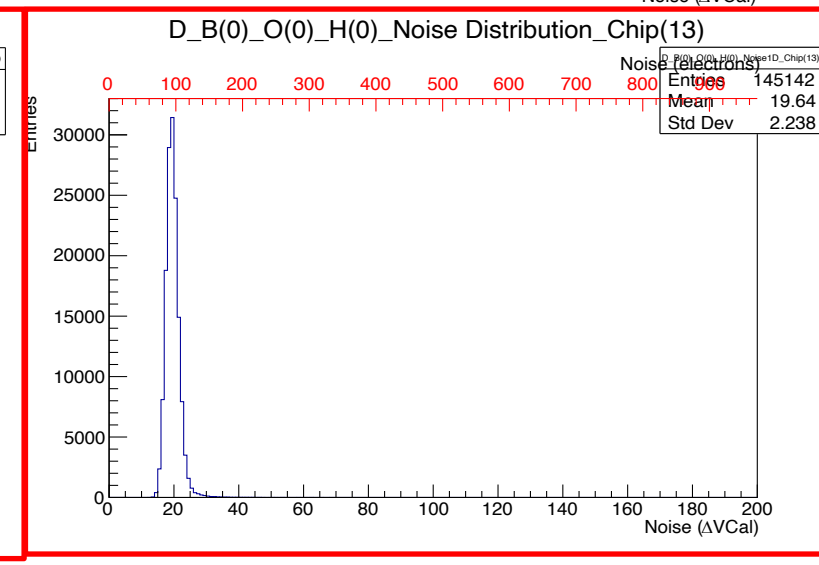
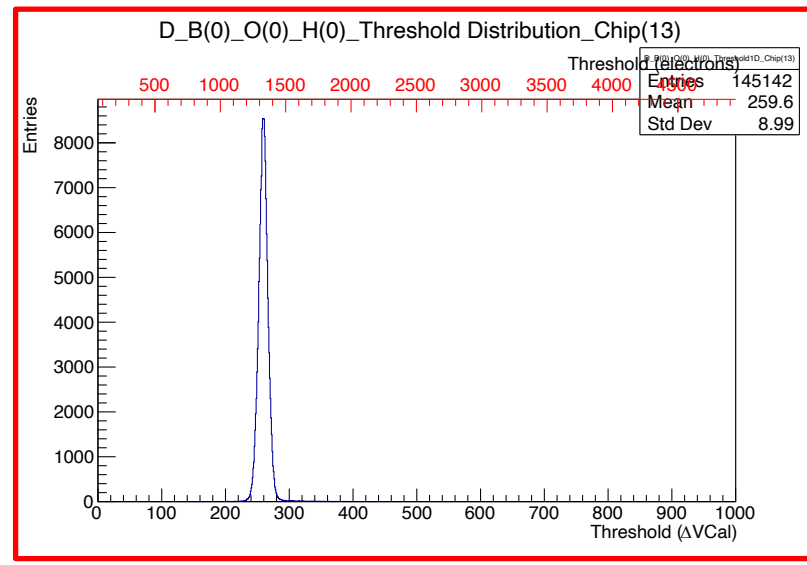
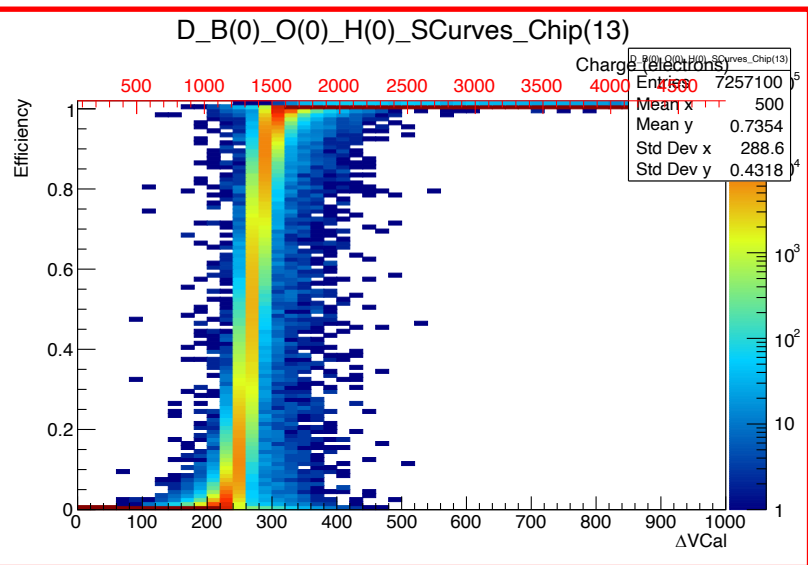
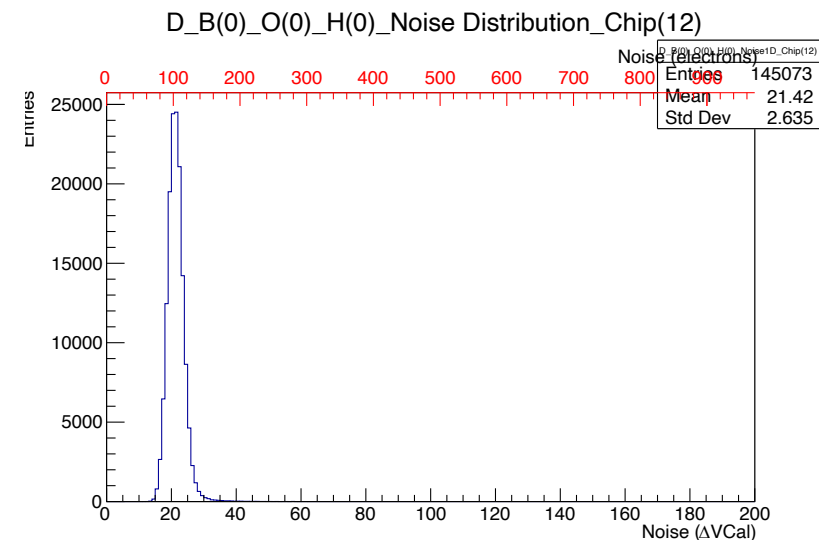
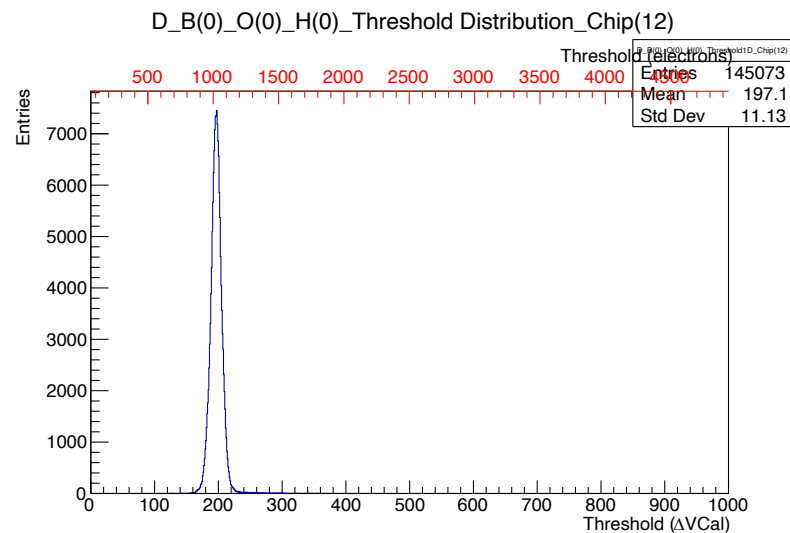
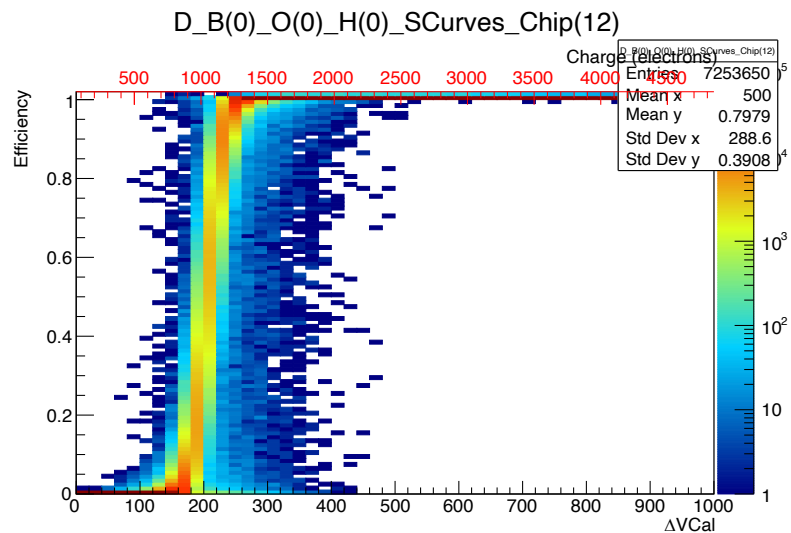


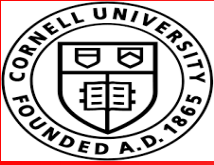
threqu @1000e-, DoNSteps=2



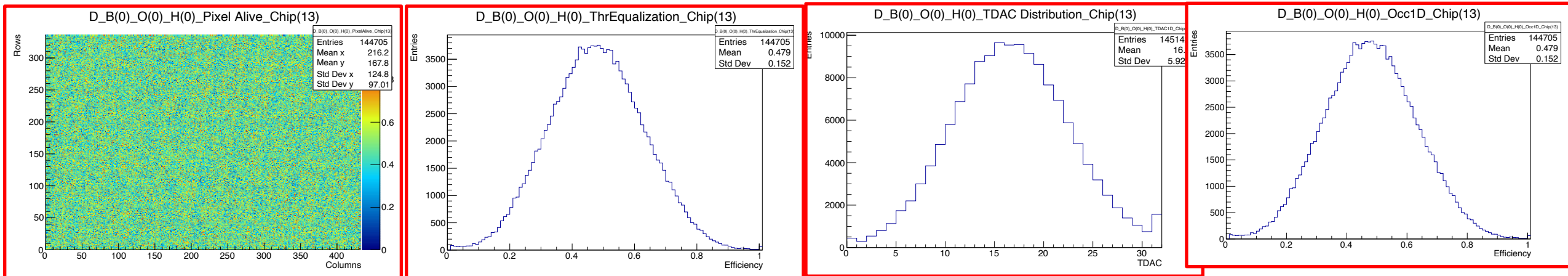
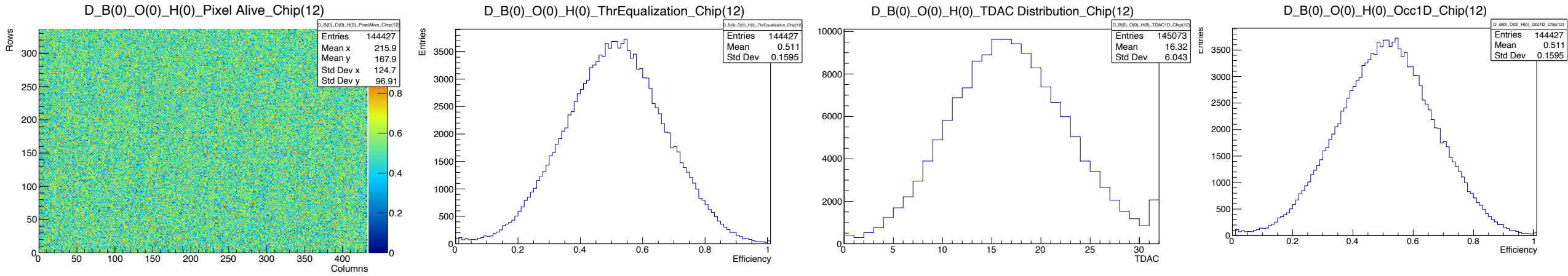


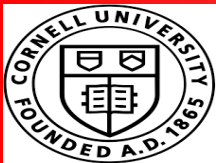
SCurve @1000e-





threqu @900e-, DoNSteps=1

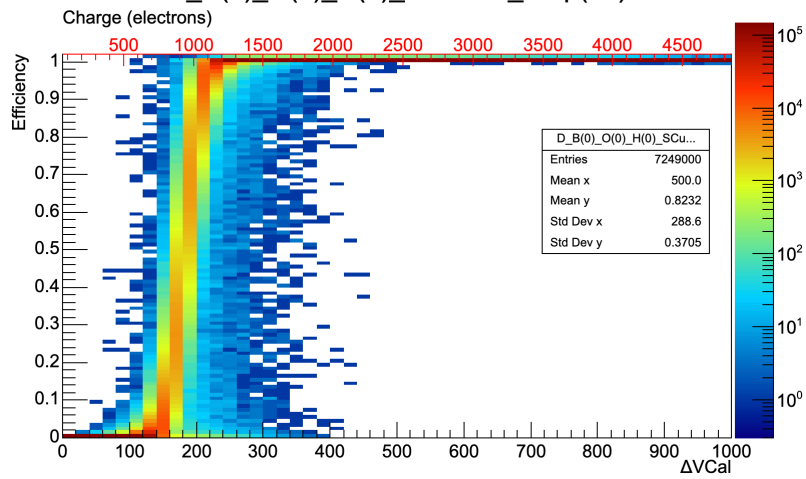




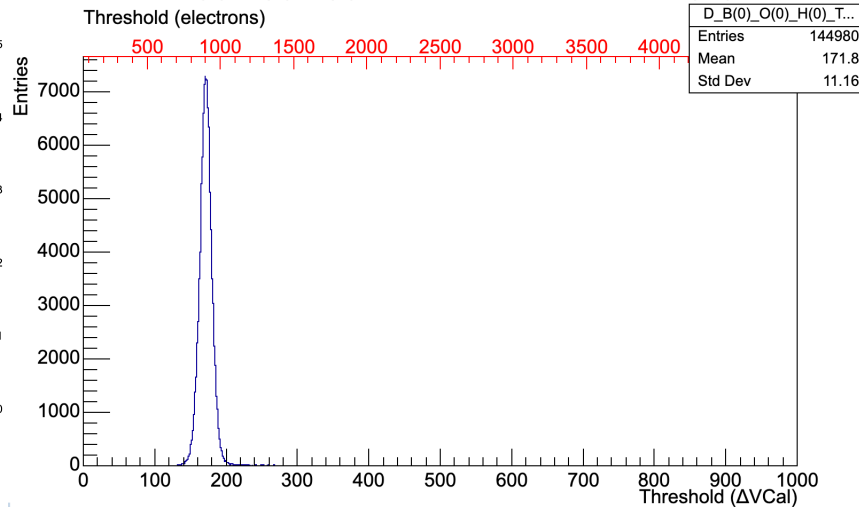
SCurve @900e-



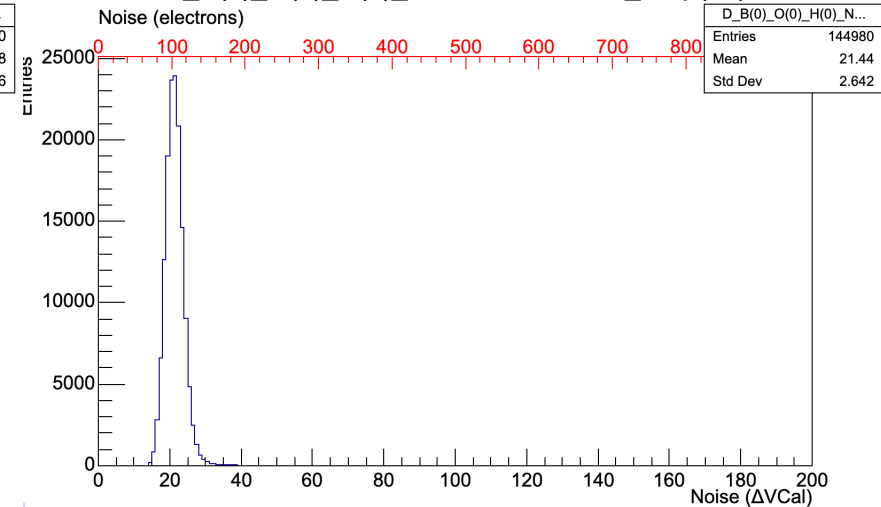
D_B(0)_O(0)_H(0)_SCurves_Chip(12)



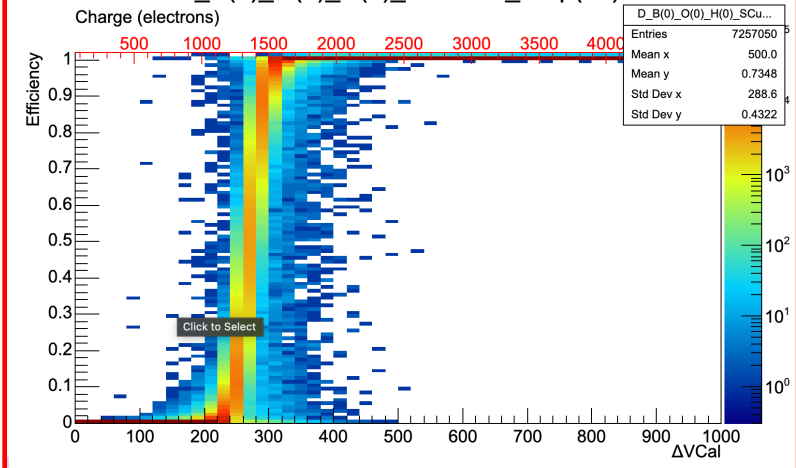
D_B(0)_O(0)_H(0)_Threshold Distribution_Chip(12)



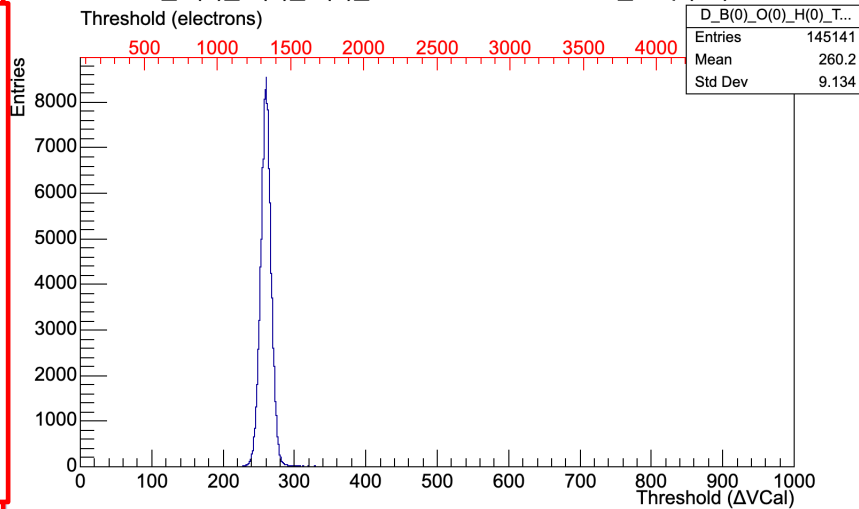
D_B(0)_O(0)_H(0)_Noise Distribution_Chip(12)



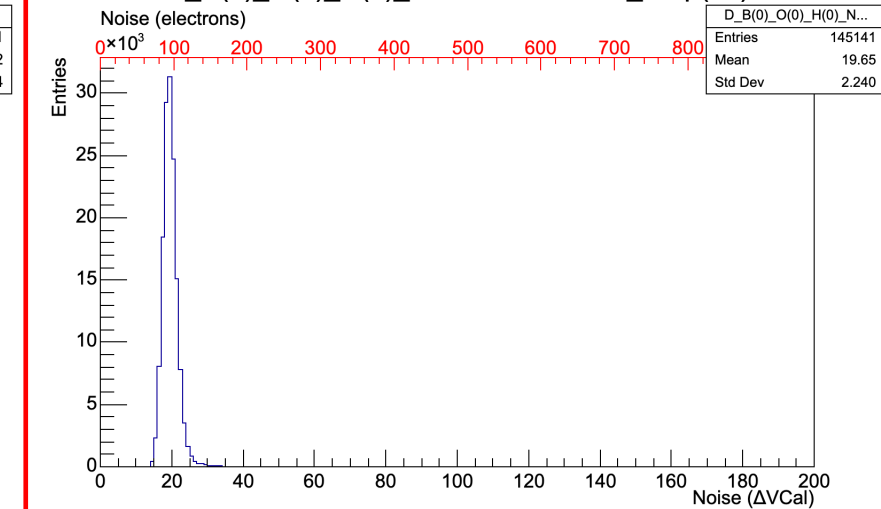
D_B(0)_O(0)_H(0)_SCurves_Chip(13)

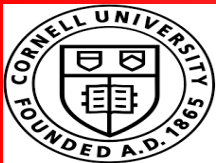


D_B(0)_O(0)_H(0)_Threshold Distribution_Chip(13)



D_B(0)_O(0)_H(0)_Noise Distribution_Chip(13)

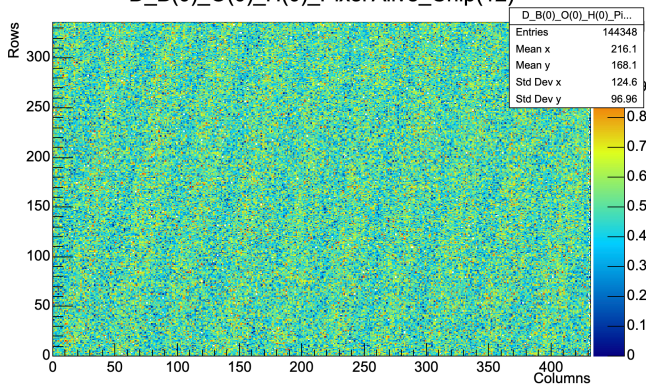




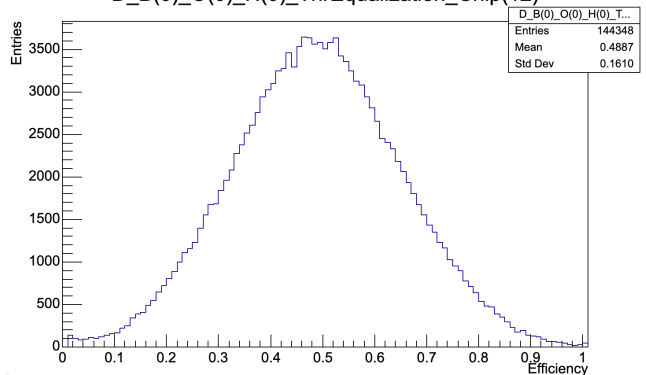
threqu @800e-, DoNSteps=1



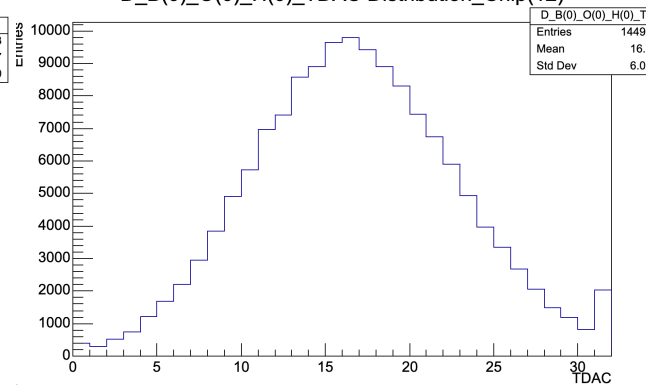
D_B(0)_O(0)_H(0)_Pixel Alive_Chip(12)



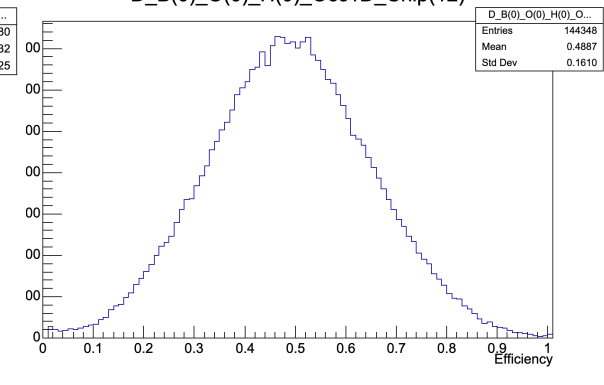
D_B(0)_O(0)_H(0)_ThrEqualization_Chip(12)



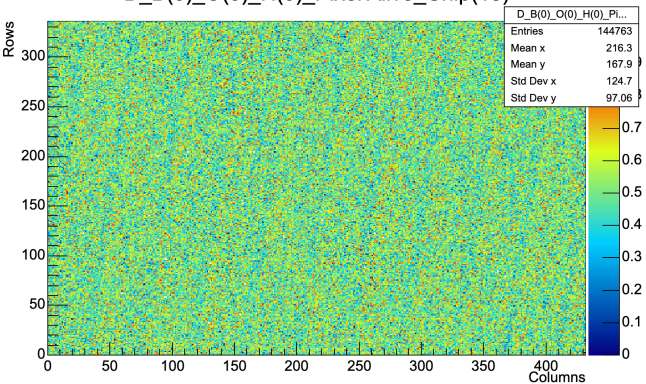
D_B(0)_O(0)_H(0)_TDAC Distribution_Chip(12)



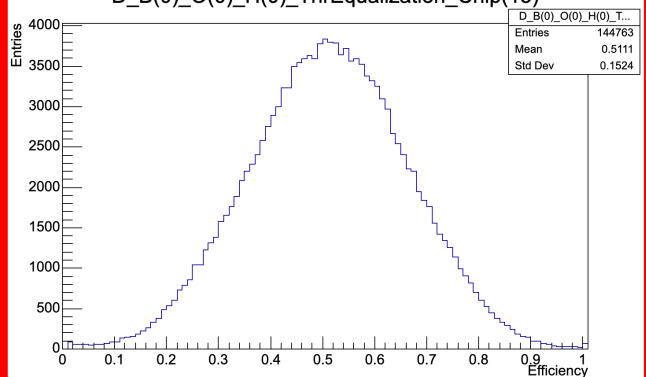
D_B(0)_O(0)_H(0)_Occ1D_Chip(12)



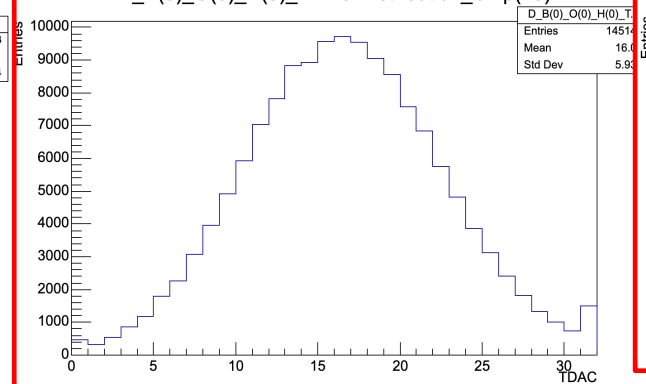
D_B(0)_O(0)_H(0)_Pixel Alive_Chip(13)



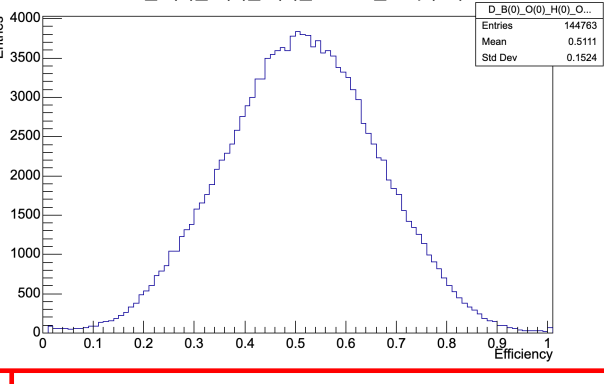
D_B(0)_O(0)_H(0)_ThrEqualization_Chip(13)

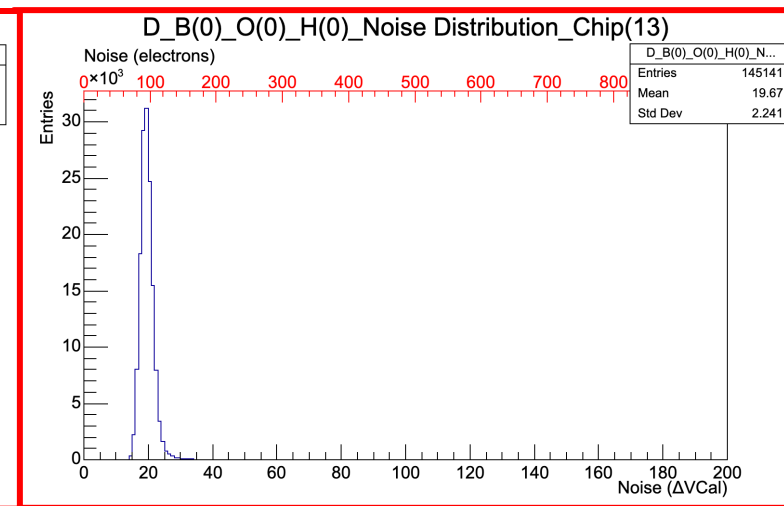
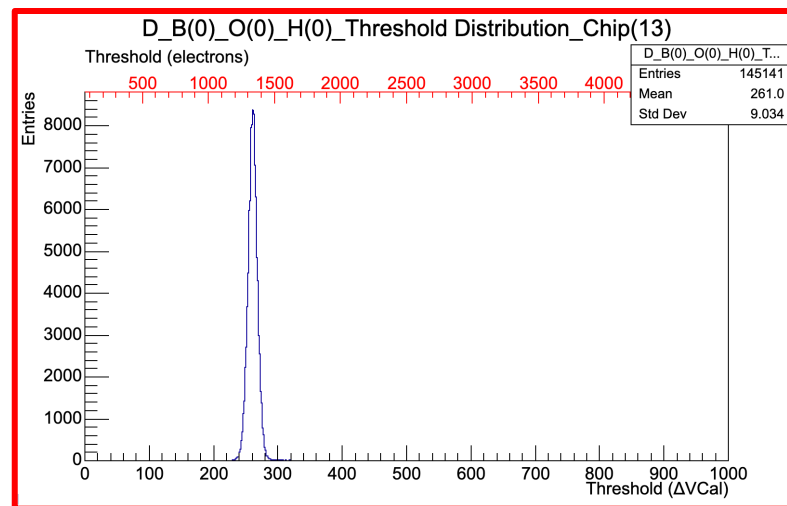
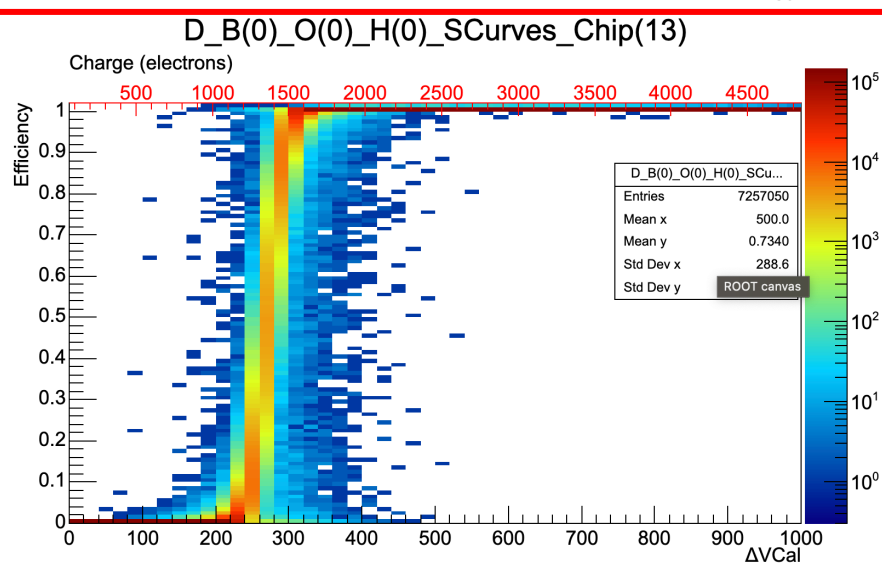
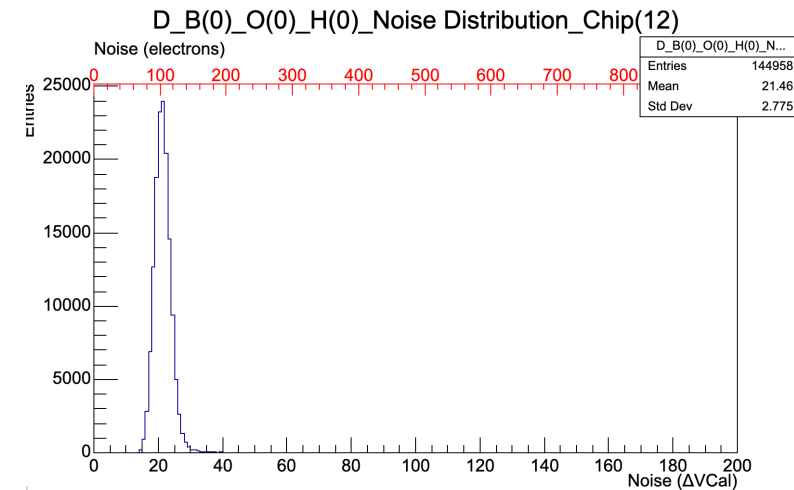
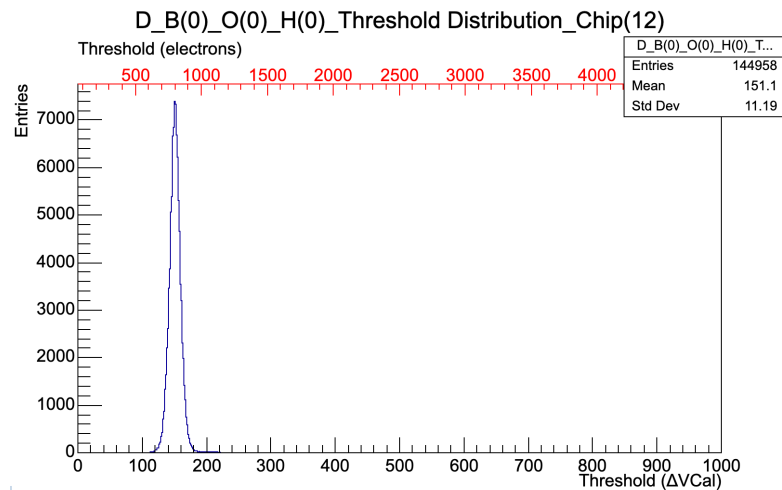
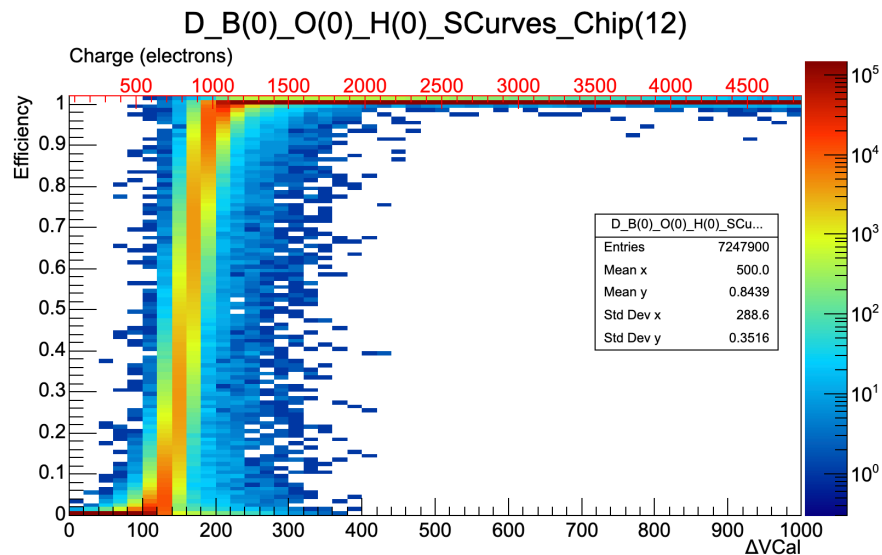


D_B(0)_O(0)_H(0)_TDAC Distribution_Chip(13)

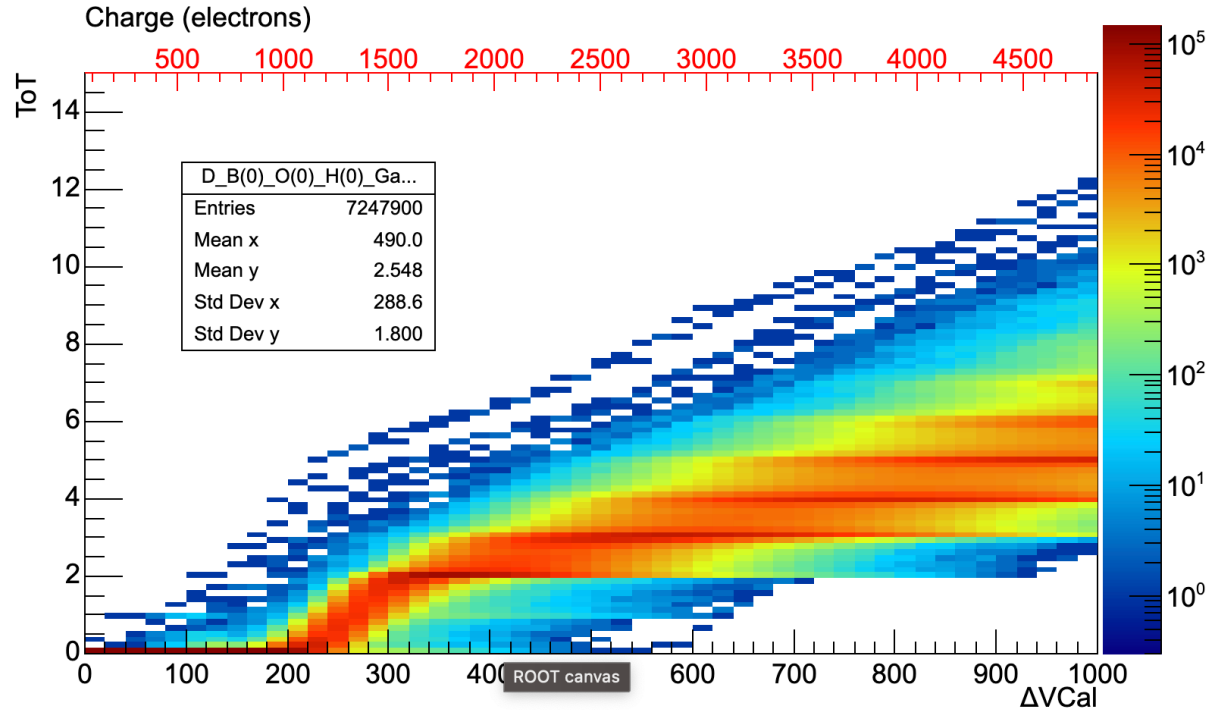


D_B(0)_O(0)_H(0)_Occ1D_Chip(13)

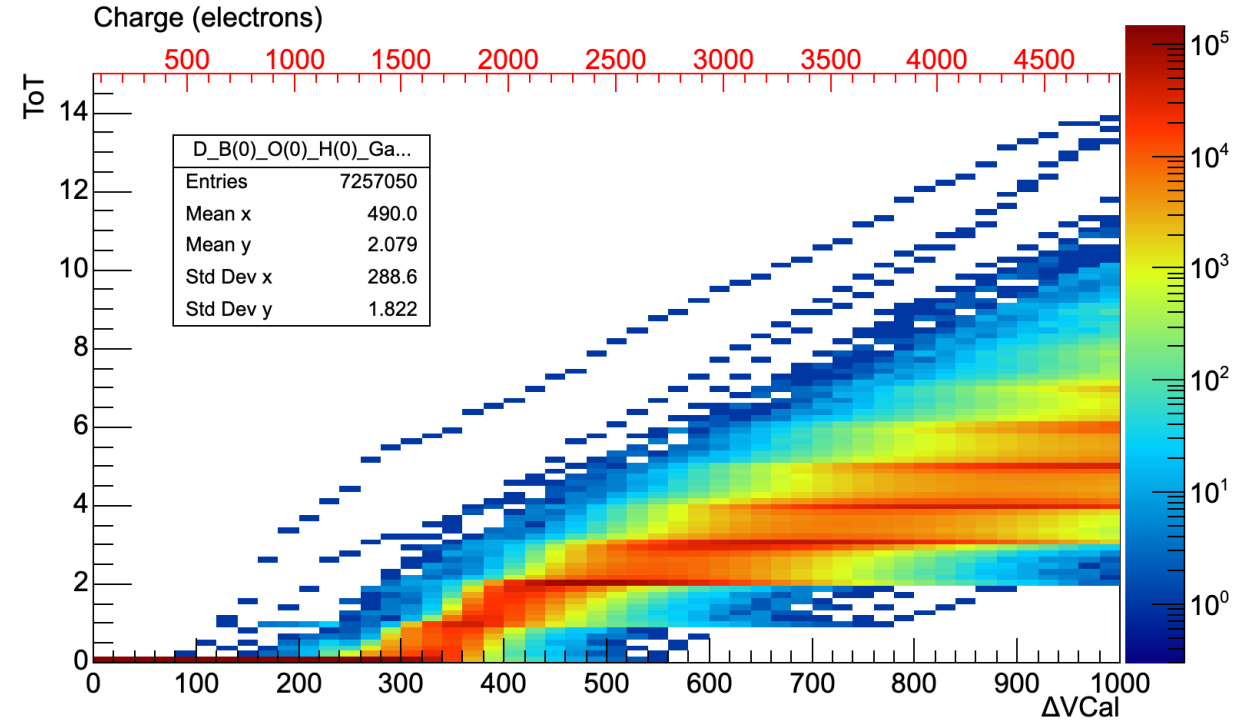




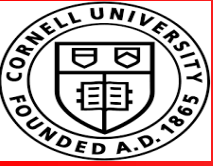
D_B(0)_O(0)_H(0)_Gain_Chip(12)



D_B(0)_O(0)_H(0)_Gain_Chip(13)



Not sure how to interpret/read this plot



- Chip 12 normal behavior
- Chip 13 is not behaving well: further investigation needed
- Next: compare with results from other sites, Xuan and Joseph.